

Elenco dei listati

E.1.1	NCO_Q.m	93
E.1.2	Imposta_frequenze_Q.m	93
E.1.3	Imposta_NCO_Q.m	94
E.1.4	Crea_super_accumulatore_Q.m	94
E.1.5	Tronca_Q.m	94
E.1.6	Crea_coseno_e_seno_Q.m	95
E.1.7	Forward_cordic_Q.m	95
E.1.8	Calcola_rotazioni_Cordic.m	96
E.1.9	Visualizza_spettro_e_SFDR_p.m	96
E.1.10	Calcola_SFDR.m	96
E.1.11	Calcola_incremento_fase_NCO.m	97
E.1.12	Visualizza_spettro_NCO_VHDL.m	97
E.2.1	CreaVettoriTest.m	97
E.2.2	CreaSequenzaPatternGenerator.m	98
E.3.1	CreaCoeffsFreqSamplScaled.m	100
E.3.2	applica_polifase.m	101
E.3.3	RaisedCosineResponse.m	102
E.3.4	CreaROM.m	102
E.3.5	PolyphasePSDVHDLvsPSDMatlab.m	104
E.3.6	applica_polifase.m	105
E.3.7	VisualizzaPSDPolifaseVHDLFPGAOout.m	105
E.3.8	applica_polifase.m	106
E.4.1	CreaTabellaBER.m	106
E.4.2	calcolaBER.m	107
E.4.3	applica_polifase.m	108
E.4.4	QPSKModemPSDDeBERVHDLvsMatlab.m	108
E.4.5	applica_polifase.m	112
E.4.6	QPSKModemPSDFPGAvsPSDMatlab.m	112
E.4.7	applica_polifase.m	114
F.1.1	nco.vhd	115
F.1.2	accumulator.vhd	116
F.1.3	cordic_pipelined_unrolled.vhd	117
F.1.4	cordic_base_j.vhd	120
F.1.5	adder_12.vhd	121

F.1.6	adder_13.vhd	121
F.1.7	reg_12.vhd	122
F.1.8	reg_13.vhd	122
F.1.9	shifter.vhd	122
F.1.10	cosine_rebuild.vhd	123
F.1.11	delay_13.vhd	123
F.1.12	sine_rebuild.vhd	123
F.1.13	to_first_quadrant.vhd	124
F.1.14	troncatore_12.vhd	124
F.2.1	polyphase_gatedClock.vhd	124
F.2.2	srrc_coeffs.vhd	125
F.2.3	rate_adapter.vhd	125
F.2.4	coeffs_selector.vhd	126
F.2.5	counter_divider_3.vhd	130
F.2.6	counter_divider_4.vhd	130
F.2.7	fftr.vhd	131
F.2.8	counter_divider_6.vhd	131
F.2.9	selector.vhd	132
F.2.10	srrc_x_n.vhd	132
F.2.11	fir_1.vhd	136
F.2.12	adder_7.vhd	138
F.2.13	fir_multiplier.vhd	139
F.2.14	shift_reg.vhd	139
F.2.15	mux_6.vhd	139
F.3.1	ROM_polyphase.vhd	140
F.3.2	counter.vhd	141
F.3.3	srrc_x_n.vhd	141
F.3.4	demux_3x10.vhd	143
F.3.5	mux_3x12.vhd	143
F.3.6	ROMx3.vhd	143
F.3.7	ROMx4.vhd	148
F.3.8	ROMx6.vhd	153
F.3.9	shift_reg.vhd	161
F.4.1	Modulator_BlockRAM.vhd	162
F.4.2	adder_I_Q.vhd	163
F.4.3	counter.vhd	164
F.4.4	multiplier_I_Q.vhd	164
F.4.5	NCO_basic.vhd	164
F.4.6	srrc_x_n.vhd	165
F.4.7	ram.vhd	166
F.4.8	shift_reg.vhd	166
F.5.1	FIFO_RAM_ThinModulator.vhd	166
F.5.2	counter.vhd	168
F.5.3	Data_Source_Interface.vhd	168

F.5.4	asynch_fifo_2x15.vhd	169
F.5.5	ffd.vhd	170
F.5.6	ffd_en.vhd	170
F.5.7	ffs.vhd	171
F.5.8	ThinModulator.vhd	171
F.5.9	mult_C2_adder.vhd	172
F.5.10	mux_2x7.vhd	173
F.5.11	ram_12x4096_rising_registered.vhd	173
F.5.12	shift_reg.vhd	174